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<input type="checkbox"/>	L11	facilitat\$.ti. and address.ti. and (email or e-mail or electronic mail) and (url or uniform resource locator) and (web adj2 page\$)		0
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<input type="checkbox"/>	L6	(url or uniform resource locator) and (web adj2 page\$) and L3 and 709/2 \$\$\$.ccls. and client and server and (metadata or meta data)		33
<input type="checkbox"/>	L5	(url or uniform resource locator) and (web adj2 page\$) and L3 and 709/2 \$\$\$.ccls.		130
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<input type="checkbox"/>	L3	(facilita\$ same access) and L1		958
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### Search Results - Record(s) 1 through 4 of 4 returned.

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1. Document ID: US 6714979 B1

L7: Entry 1 of 4

File: USPT

Mar 30, 2004

US-PAT-NO: 6714979

DOCUMENT-IDENTIFIER: US 6714979 B1

TITLE: Data warehousing infrastructure for web based reporting tool

<a href="#">Full</a>	<a href="#">Title</a>	<a href="#">Citation</a>	<a href="#">Front</a>	<a href="#">Review</a>	<a href="#">Classification</a>	<a href="#">Date</a>	<a href="#">Reference</a>	<a href="#">Sequences</a>	<a href="#">Attachments</a>	<a href="#">Claims</a>	<a href="#">KIMC</a>	<a href="#">Drawn De</a>
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2. Document ID: US 6631402 B1

L7: Entry 2 of 4

File: USPT

Oct 7, 2003

US-PAT-NO: 6631402

DOCUMENT-IDENTIFIER: US 6631402 B1

TITLE: Integrated proxy interface for web based report requester tool set

<a href="#">Full</a>	<a href="#">Title</a>	<a href="#">Citation</a>	<a href="#">Front</a>	<a href="#">Review</a>	<a href="#">Classification</a>	<a href="#">Date</a>	<a href="#">Reference</a>	<a href="#">Sequences</a>	<a href="#">Attachments</a>	<a href="#">Claims</a>	<a href="#">KIMC</a>	<a href="#">Drawn De</a>
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3. Document ID: US 6615258 B1

L7: Entry 3 of 4

File: USPT

Sep 2, 2003

US-PAT-NO: 6615258

DOCUMENT-IDENTIFIER: US 6615258 B1

TITLE: Integrated customer interface for web based data management

<a href="#">Full</a>	<a href="#">Title</a>	<a href="#">Citation</a>	<a href="#">Front</a>	<a href="#">Review</a>	<a href="#">Classification</a>	<a href="#">Date</a>	<a href="#">Reference</a>	<a href="#">Sequences</a>	<a href="#">Attachments</a>	<a href="#">Claims</a>	<a href="#">KIMC</a>	<a href="#">Drawn De</a>
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4. Document ID: US 6377993 B1

L7: Entry 4 of 4

File: USPT

Apr 23, 2002

US-PAT-NO: 6377993

DOCUMENT-IDENTIFIER: US 6377993 B1

TITLE: Integrated proxy interface for web based data management reports

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Search Results - Record(s) 1 through 16 of 16 returned.

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1. Document ID: US 6654869 B1

L9: Entry 1 of 16

File: USPT

Nov 25, 2003

US-PAT-NO: 6654869

DOCUMENT-IDENTIFIER: US 6654869 B1

\*\* See image for Certificate of Correction \*\*

TITLE: Assigning a group tag to an instruction group wherein the group tag is recorded in the completion table along with a single instruction address for the group to facilitate in exception handling

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KMM](#) [Drawn D](#)

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2. Document ID: US 6654793 B1

L9: Entry 2 of 16

File: USPT

Nov 25, 2003

US-PAT-NO: 6654793

DOCUMENT-IDENTIFIER: US 6654793 B1

TITLE: System and method for facilitating dynamic loading of stub information to enable a program operating in one address space to invoke processing of a remote method or procedure in another address space

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KMM](#) [Drawn D](#)

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3. Document ID: US 6557037 B1

L9: Entry 3 of 16

File: USPT

Apr 29, 2003

US-PAT-NO: 6557037

DOCUMENT-IDENTIFIER: US 6557037 B1

\*\* See image for Certificate of Correction \*\*

TITLE: SYSTEM AND METHOD FOR EASING COMMUNICATIONS BETWEEN DEVICES CONNECTED RESPECTIVELY TO PUBLIC NETWORKS SUCH AS THE INTERNET AND TO PRIVATE NETWORKS BY FACILITATING RESOLUTION OF HUMAN-READABLE ADDRESSES

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KMM](#) [Drawn D](#)

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4. Document ID: US 6553222 B1

L9: Entry 4 of 16

File: USPT

Apr 22, 2003

US-PAT-NO: 6553222

DOCUMENT-IDENTIFIER: US 6553222 B1

TITLE: Method and system facilitating automatic address book entries with caller specific voice identifiers and call notification[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KWMC](#) [Drawn D](#) 5. Document ID: US 6473431 B1

L9: Entry 5 of 16

File: USPT

Oct 29, 2002

US-PAT-NO: 6473431

DOCUMENT-IDENTIFIER: US 6473431 B1

\*\* See image for Certificate of Correction \*\*TITLE: System and method facilitating determination by router nodes in a network of ranges of addresses for which each router node is responsible[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KWMC](#) [Drawn D](#) 6. Document ID: US 6377161 B1

L9: Entry 6 of 16

File: USPT

Apr 23, 2002

US-PAT-NO: 6377161

DOCUMENT-IDENTIFIER: US 6377161 B1

TITLE: Method and apparatus in a wireless messaging system for facilitating an exchange of address information[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KWMC](#) [Drawn D](#) 7. Document ID: US 6192424 B1

L9: Entry 7 of 16

File: USPT

Feb 20, 2001

US-PAT-NO: 6192424

DOCUMENT-IDENTIFIER: US 6192424 B1

TITLE: Bus arbiter for facilitating access to a storage medium in enhanced burst mode using freely specifiable address increments/decrements[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KWMC](#) [Drawn D](#)

8. Document ID: US 6076176 A

L9: Entry 8 of 16

File: USPT

Jun 13, 2000

US-PAT-NO: 6076176

DOCUMENT-IDENTIFIER: US 6076176 A

TITLE: Encoding of failing bit addresses to facilitate multi-bit failure detect using a wired-OR scheme[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KIMC](#) | [Drawn](#) 9. Document ID: US 5991866 A

L9: Entry 9 of 16

File: USPT

Nov 23, 1999

US-PAT-NO: 5991866

DOCUMENT-IDENTIFIER: US 5991866 A

TITLE: Method and system for generating a program to facilitate rearrangement of address bits among addresses in a massively parallel processor system[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KIMC](#) | [Drawn](#) 10. Document ID: US 5933858 A

L9: Entry 10 of 16

File: USPT

Aug 3, 1999

US-PAT-NO: 5933858

DOCUMENT-IDENTIFIER: US 5933858 A

TITLE: Efficient internal address encoding scheme for an integrated circuit which facilitates multiple addressing modes[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KIMC](#) | [Drawn](#) 11. Document ID: US 5911153 A

L9: Entry 11 of 16

File: USPT

Jun 8, 1999

US-PAT-NO: 5911153

DOCUMENT-IDENTIFIER: US 5911153 A

TITLE: Memory design which facilitates incremental fetch and store requests off applied base address requests[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KIMC](#) | [Drawn](#) 12. Document ID: US 5572713 A

L9: Entry 12 of 16

File: USPT

Nov 5, 1996

US-PAT-NO: 5572713  
DOCUMENT-IDENTIFIER: US 5572713 A

TITLE: System and method for obtaining correct byte addresses by using logical operations on 2 least significant bits of byte address to facilitate compatibility between computer architectures having different memory orders

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KIMC](#) [Drawn](#) [De](#)

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13. Document ID: US 5564057 A

L9: Entry 13 of 16

File: USPT

Oct 8, 1996

US-PAT-NO: 5564057  
DOCUMENT-IDENTIFIER: US 5564057 A

TITLE: Microprocessor architecture which facilitates input/output utilizing pairs of registers which the same address

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KIMC](#) [Drawn](#) [De](#)

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14. Document ID: US 5479632 A

L9: Entry 14 of 16

File: USPT

Dec 26, 1995

US-PAT-NO: 5479632  
DOCUMENT-IDENTIFIER: US 5479632 A

TITLE: Microcomputer having two-level memory to facilitate calculation of effective addresses

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KIMC](#) [Drawn](#) [De](#)

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15. Document ID: US 5398328 A

L9: Entry 15 of 16

File: USPT

Mar 14, 1995

US-PAT-NO: 5398328  
DOCUMENT-IDENTIFIER: US 5398328 A  
\*\* See image for Certificate of Correction \*\*

TITLE: System for obtaining correct byte addresses by XOR-ING 2 LSB bits of byte address with binary 3 to facilitate compatibility between computer architecture having different memory orders

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KIMC](#) [Drawn](#) [De](#)

16. Document ID: US 4989182 A

L9: Entry 16 of 16

File: USPT

Jan 29, 1991

US-PAT-NO: 4989182

DOCUMENT-IDENTIFIER: US 4989182 A

TITLE: Dynamic random access memory having dummy word line for facilitating reset of row address latch[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KIMC](#) [Drawn](#) [Def](#)[Clear](#) [Generate Collection](#) [Print](#) [Fwd Refs](#) [Bkwd Refs](#) [Generate OACS](#)

Term	Documents
ADDRESS	259261
ADDRESSES	151474
FACILITAT\$	0
FACILITAT	14
FACILITATA	1
FACILITATABLE	11
FACILITATAD	1
FACILITATAE	2
FACILITATAED	1
FACILITATATE	2
FACILITATATED	1
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